RESPONSE UNDER 37 C.F.R. § 1.116 EXPEDITED PROCEDURE **EXAMINING GROUP 2800**



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Facsimile Number: 571-273-8300

Transmission Date:

March 5, 2007

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Docket No.:

2004 P 51343 US

Serial No:

10/840.146

Art Unit:

2814

Date Filed:

May 6:2004

Examiner:

Shrinivas H. Rao

Title:

Semiconductor Structures and Manufacturing Methods

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- Response under 37 CFR § 1.116 (13 pages)

Respectfully submitted,

Legal Assistant

Confirmation Respectfully Requested

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Rao, Shrinivas H.

For:

Semiconductor Structures and Manufacturing Methods

Mail Stop: AF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

RESPONSE UNDER 37 CFR § 1.116

Dear Sir:

Applicants respectfully submit the following amendments and remarks in response to Examiner's Office Action dated January 5, 2007, which Action is made final. Applicants respectfully request that these amendments and remarks be entered in pursuant to the provisions of 37 CFR § 1.116, and respectfully request reconsideration of claims 5-18.